

DIGITAL INPUT/OUTPUT CARD

IOD-24E

USER MANUAL

File: MIOD-24E.B2c

NOTICES

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INSTALLATION

The software provided with this card is contained on either one CD or multiple diskettes and must be installed onto your hard disk prior to use. To do this, perform the following steps as appropriate for your software format and operating system. Substitute the appropriate drive letter for your CD-ROM or disk drive where you see D: or A: respectively in the examples below.

CD INSTALLATION

DOS/WIN3.x

- 1. Place the CD into your CD-ROM drive.
- 2. Type Different to change the active drive to the CD-ROM drive.
- 3. Type INSTALLE to run the install program.
- 4. Follow the on-screen prompts to install the software for this card.

WIN95/98/NT

- 1. Place the CD into your CD-ROM drive.
- 2. The CD should automatically run the install program after 30 seconds. If the install program does not run, click START | RUN and type DINSTALL, click OK or press EMP.
- 3. Follow the on-screen prompts to install the software for this card.
- 4. Click the "Go to ACCES Web" button to check for software updates.

3.5-INCH DISKETTE INSTALLATION

As with any software package, you should make backup copies for everyday use and store your original master diskettes in a safe location. The easiest way to make a backup copy is to use the DOS DISKCOPY utility.

In a single-drive system, the command is:

DISKCOPY A: A:Enter

You will need to swap disks as requested by the system. In a two-disk system, the command is:

DISKCOPY A: B:Enter

This will copy the contents of the master disk in drive A to the backup disk in drive B.

To copy the files on the master diskette to your hard disk, perform the following steps.

- 1. Place the master diskette into a floppy drive
- 2. Change the active drive to the drive that has the diskette installed. For example, if the diskette is in drive A, type A:
- 3. Type INSTALLE and follow the on-screen prompts.

DIRECTORIES CREATED ON THE HARD DISK

The installation process will create several directories on your hard disk. If you accept the installation defaults, the following structure will exist.

[CARDNAME] Root or base directory containing the SETUP.EXE setup program used to help you configure jumpers and calibrate the card.

DOS\PSAMPLES: A subdirectory of [CARDNAME] that contains Pascal samples.

- **DOS\CSAMPLES:** A subdirectory of [CARDNAME] that contains "C" samples.
- WIN32*language* Subdirectories containing samples for Win95/98 and NT.
 - WinRisc.exe: A Windows dumb-terminal type communication program designed for RS422/485 operation. Used primarily with REMOTE ACCES Data Acquisition Pods and our RS422/485 serial communication product line. Can be used to say hello to an installed modem.
- ACCES32: This directory contains the Windows 95/98/NT driver used to provide access to the hardware registers when writing 32-bit Windows software. Several samples are provided in a variety of languages to demonstrate how to use this driver. The DLL provides four functions (InPortB, OutPortB, InPort, and OutPort) to access the hardware.

This directory also contains the device driver for Windows NT, ACCESNT.SYS. This device driver provides register-level hardware access in Windows NT. Two methods of using the driver are available, through ACCES32.DLL (recommended) and through the DeviceIOControl handles provided by ACCESNT.SYS (slightly faster).

SAMPLES: Samples for using ACCES32.DLL are provided in this directory. Using this DLL not only makes the hardware programming easier (MUCH easier), but also one source file can be used for both Windows 95/98 and

WindowsNT. One executable can run under both operating systems and still have full access to the hardware registers. The DLL is used exactly like any other DLL, so it is compatible with any language capable of using 32-bit DLLs. Consult the manuals provided with your language's compiler for information on using DLLs in your specific environment.

- VBACCES: This directory contains sixteen-bit DLL drivers for use with VisualBASIC 3.0 and Windows 3.1 only. These drivers provide four functions, similar to the ACCES32.DLL. However, this DLL is only compatible with 16-bit executables. Migration from 16-bit to 32-bit is simplified because of the similarity between VBACCES and ACCES32.
- **PCI:** This directory contains PCI-bus specific programs and information. If you are not using an ACCES PCI card, this directory will not be installed.
 - **SOURCE**: A utility program is provided with source code you can use to determine allocated resources at run-time from your own programs in DOS.
 - PCIFind.exe A utility for DOS and Windows to determine what base address and IRQ are allocated to installed PCI cards. This program runs two versions, depending on the operating system. Windows 95/98/NT displays a GUI interface, and modifies the registry. When run from DOS or Windows3.x, a text interface is used. For information about the format of the registry key, consult the card-specific samples provided with the hardware. In Windows NT, NTioPCI.SYS runs each time the computer is booted, thereby refreshing the registry as PCI hardware is added or removed. In Windows 95/98/NT PCIFind.EXE places itself in the bootsequence of the OS to refresh the registry on each power-up.

This program also provides some COM configuration when used with PCI COM ports. Specifically, it will configure compatible COM cards for IRQ sharing and multiple port issues.

- WIN32IRQ: This directory provides a generic interface for IRQ handling in Windows 95/98/NT. Source code is provided for the driver, greatly simplifying the creation of custom drivers for specific needs. Samples are provided to demonstrate the use of the generic driver. Note that the use of IRQs in near-real-time data acquisition programs requires multi-threaded application programming techniques and must be considered an intermediate to advanced programming topic. Delphi, C++ Builder, and Visual C++ samples are provided.
- Findbase.exe DOS utility to determine an available base address for ISA bus, non-Plug-n-Play cards. Run this program once, before the hardware is installed in the computer, to determine an available address to give the card. Once the address has been determined, run the setup program provided with the hardware to see instructions on setting the address switch and various option selections.
- Poly.exe A generic utility to convert a table of data into an nth order polynomial. Useful for calculating linearization polynomial coefficients for thermocouples and other non-linear sensors.
- Risc.bat A batch file demonstrating the command line parameters of RISCTerm.exe.
- RISCTerm.exe A dumb-terminal type communication program designed for RS422/485 operation. Used primarily with REMOTE ACCES Data Acquisition Pods and our RS422/485 serial communication product line. Can be used to say hello to an installed modem. RISCTerm stands for Really Incredibly Simple Communications TERMinal.

INSTALLING THE CARD

Before installing the card carefully read the ADDRESS SELECTION and OPTION SELECTION Sections of this manual and configure the card according to your requirements. Use the special software program called SETUP provided on the CD with the card. It supplies visual aids to configure all areas of the board.

Be especially careful with address selection. If the addresses of two installed functions overlap, you will experience unpredictable computer behavior. If unsure what locations are available, you can use the FINDBASE program provided to locate blocks of available addresses.

To install the card:

- 1. Remove power from the computer.
- 2. Remove the computer cover.
- 3. Remove blank I/O backplate.
- 4. Install jumpers for selected options. See OPTION SELECTION
- 5. Select the base address on the card. See ADDRESS SELECTION
- 6. Install the card in an I/O expansion slot.
- 7. Inspect for proper fit of the card and cables and tighten screws.
- 8. Replace the computer cover.

A 50-pin connector is provided on the mounting bracket for input/output connections. To ensure that there is minimum susceptibility to EMI and minimum radiation, it is important that the card mounting bracket be properly screwed into place and that there be a positive chassis ground. Also, proper EMI cabling techniques (cable connect to chassis ground at the aperture, shielded twisted-pair wiring, etc) must be used for the input and output wiring.

FUNCTIONAL DESCRIPTION

FEATURES

24 Input/Output Lines Four and Eight Bit Groups Independently Selectable for I/O Bi-Directional Data Transfers Supported. Supports PPI Modes 0, 1, and 2 Interrupt and Interrupt-Disable Capability OPTO-22 Compatible 50-Pin Connector Compatibility with Module Mounting Racks +5 V Supply Available to User

APPLICATIONS

Automatic Test Systems Security Systems, Energy Management Relay Monitoring and Control Robotics Parallel Data Transfer to PC Sensing Switches or Signals, or TTL, DTL, CMOS Logic

DESCRIPTION

Model IOD-24E is an economical 24-bit parallel digital input/output card. It is half size and can be installed in either long or short expansion slots of IBM PC/XT/AT or compatible computers.

The card contains a Programmable Peripheral Interface chip type 8255-5 (PPI) which provides computer interface to 24 digital I/O lines. Three 8-bit ports A, B, and C can be configured to function as either inputs, or as output latches. Port C can be also configured as four inputs and four output latches. All 8255-5 PPI modes are supported by the IOD-24E.

Two I/O lines can be used for interfacing User Interrupts to the computer. Interrupts are buffered and are enabled by a digital input line. You can use Interrupt levels #2 through #7.

Input/Output wiring connections are via a 50-pin connector at the rear of the computer. This provides compatibility with industry-standard module mounting racks from OPTO-22, Gordos, Potter & Brumfield, etc. Also, the connections are compatible with ACCES model STA-50 Screw Terminal Accessory Boards. Every second conductor of the flat cable is grounded to minimize the effect of crosstalk between signals.

If needed for external circuits, +5 VDC power is available at connector pin 49.

The IOD-24E occupies four consecutive bytes of I/O address space. The base address is selectable via a DIP switch anywhere within the range of 000-3FF hex. If in doubt how to select a base address, check your computer reference manual. For additional information about setting the base address of IOD-24E, see the ADDRESS SELECTION section of this manual and the setup software program supplied with the card.

Utility software is provided with the IOD-24E card. This software includes an illustrated setup program, and sample programs in Quickbasic, C and Pascal.

IOD-24E BLOCK DIAGRAM



OPTION SELECTION

Refer to the OPTION SELECTION MAP on the next page, and the setup program on the CD provided with the card when reading this section of the manual.

SLOT 8

Install this jumper when the card is located in a short slot of the host computer.

WAIT

Install this jumper when the host computer clock speed is 8Mhz or more. This will automatically assert one wait state when the card is addressed.

INT 2,3,4,5,6,7

Install the appropriate jumper to select the interrupt level that the card will be working at.

The foregoing are the only manual setups necessary to use the IOD-24E. Input/Output selection is done via <u>software</u> by writing to a control register in the PPI as described in the PROGRAMMING section of this manual.

ADDRESS SELECTION

The IOD-24E Digital Input/Output Card occupies four bytes of I/O address space. The base address can be selected anywhere within an I/O address range 100-3FC hex in AT's (except 1F0 through 1F8) and 200-3F0 in XT's. However, two installed options cannot share the same address. If in doubt where to assign the base address, refer to the following tables and the FINDBASE program to find an available address for your card.

STANDARD ADDRESS ASSIGNMENTS FOR 286/386/486 COMPUTERS

Hex Range	Usage
000-01F	DMA Controller 1
020-03F	INT Controller 1. Master
040-05F	Timer
060-06F	8042 (Keyboard)
070-07F	Real Time Clock, NMI Mask
080-09F	DMA Page Register
0A0-0BF	INT Controller 2
0C0-0DF	DMA Controller 2
0F0	Clear Math Coprocessor Busy
0F1	Reset Coprocessor
0F8-0FF	Arithmetic Processor
1F0-1F8	Fixed Disk
200-207	Game I/O
278-27F	Parallel Printer Port 2
2F8-2FF	Asynchronous Comm'n (Secondary)
300-31F	Prototype Card
360-36F	Reserved
378-37F	Parallel Printer Port 1
380-38F	SDLC or Binary Synchronous Comm'n 2
3A0-3AF	Binary Synchronous Comm'n 1
3B0-3BF	Monochrome Display/Printer
3C0-3CE	Local Area Network
3D0-3DF	Color/Graphic Monitor
3F0-3F7	Floppy Diskette Controller
3F8-3FF	Asynchronous Comm'n (Primary)

ADDRESS SETUP switches are marked A2-A9. In order to configure the desired address, assign '1' to all ADDRESS SETUP switches turned OFF, and assign '0' to all ADDRESS SETUP switches turned ON. These 1's and 0's are a binary representation of the base address. This binary representation is then converted to hexadecimal.

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Address Line Controlled A9 A8 A7 A6 A5	A4	A3	A2
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The following example illustrates switch selection corresponding to hex 2DC (or binary 10 1101 11xx). The "xx" represents address lines A1, and A0 used on the card to select individual registers at the PPI as described in Section 6, PROGRAMMING.

Switch ID	A9	A8	A7	A6	A5	A4	A3	A2
Setup	OFF	ON	OFF	OFF	ON	OFF	OFF	OFF
Binary Representation	1	0	1	1	0	1	1	1
Conversion Multipliers	2	1	8	4	2	1	8	4
Hex Representation	2		D			С		

Carefully review the address selection reference Table on the preceding page before selecting the card address. If the addresses of two installed functions overlap, you will experience unpredictable computer behavior.

SOFTWARE

ACCES supplies several programs to support the IOD-24E digital I/O card and, also, to help you to develop your applications program. The sample programs are in forms suitable for use with, QuickBASIC, C, and Pascal. The programs as follows:

FINDBASE: Program locates active and available port addresses.

SETUI	P: IOD-1	20 Board Setup Program
BSAMPLES:	SAMPLE1	A Quickbasic program that writes a sequence of values to Port A and reads and displays the values in Ports A & B.
	SAMPLE2	A Quickbasic program that displays the bits in Ports A & B and, when an interrupt occurs, polls those same bits.
CSAMPLES:	SAMPLE1	A "C" program that writes a sequence of values to Port A and reads and displays the values in Ports A & B.
	SAMPLE2	A "C" program that displays the bits in Ports A & B and, when an interrupt occurs, polls those same bits.
	EVENT	A sample program demonstrating a security system that monitors the status of 16 switches and automatically triggers alarms.
PSAMPLES:	SAMPLE1	A "Pascal" program that writes a sequence of values to Port A and reads and displays the values in Ports A & B.
	EVENT	A sample program demonstrating a security system that monitors the status of 16 switches and automatically triggers alarms. These programs are on a CD that comes with your board.

SETUP

This program is supplied with the IOD-24E as a tool for you to use in configuring the jumpers on the card. It is a menu-driven program that provides illustrations. You make simple keystrokes to select functions. The illustrations then show how to place the jumpers.

The setup program is a stand-alone program that can be run at any time. It does not require the IOD-24E to be plugged into the computer for any part of the setup. The program is self explanatory with operation instructions and on-line help.

PROGRAMMING

The IOD-24E is an I/O mapped device that is easily configured from any language and any language can easily perform digital I/O through the card's ports. This is especially true if the form of the data is Byte or Word wide. All references to the I/O ports would be in absolute port addressing. However, a table could be used to convert the Byte or Word data ports to a logical reference.

DEVELOPING YOUR APPLICATION SOFTWARE

If you wish to gain a better understanding of the software in the previous section, then the information in the following paragraphs will be of interest to you. Follow the 8255-5 PPI specification to program the PPI on the IOD-24E card.

The PPI is addressed using address bits A1 and A0 (See Address Selection section of this manual.) as follows:

ADDRESS	PORT ASSIGNMENT	OPERATION
Base Address	PA Port	Read/Write
Base Address +1	PB Port	Read/Write
Base Address +2	PC Port	Read/Write
Base Address +3	Control Register	Write Only

REGISTER ADDRESS ASSIGNMENTS

The IOD-24E card supports all three operating modes of the 8255-5 PPI.

Mode 0 is the most frequently used mode of operation wherein:

- a. There are two 8-bit ports (A and B) and two 4-bit ports (C Hi and C Lo).
- b. Any port can be configured as an input or an output.
- c. Outputs are latched.
- d. Inputs are not latched.

Mode 1 is used for strobed input/output. In this mode Port A and Port B use the lines of Port C to generate and accept control signals associated with data transfer. See the 8255-5 PPI specification for control line assignments.

Mode 2 is used for bi-directional eight-bit bus applications. See the 8255-5 specification and consult factory if this mode of operation is desired.

The control register at base address +3 is a write-only 8-bit register. It is used to set the mode and direction of the ports. At Power-Up or Reset, all I/O lines are set as inputs. The PPI should be configured during initializing by writing to the control register even if the ports are only going to be used as inputs. Bit assignments in the control register are as follows:

Bit	Assignment	Function
D0 D1 D2 D3 D4 D5,D6	Port C Lo (C0-C3) Port B Mode Selection Port C Hi (C4-C7) Port A Mode Selection	1 = Input, 0 = Output 1 = Input, 0 = Output 1 = Mode 1, 0 = Mode 0 1 = Input, 0 = Output 1 = Input, 0 = Output 00 = Mode 0, 01 = Mode 1 1X = Mode 2
D7	Mode Set Flag	1 = Active

CONTROL REGISTER BIT ASSIGNMENTS

(Continued on next page)

IOD-24E

PROGRAMMING EXAMPLE

The following example in BASIC is provided as a guide to assist you in developing your working software. In this example, the card address is 2D0 hex, operation is to be in Mode 0 and the I/O lines are to be set up as follows:



This corresponds to 98 hex. If the card base address is 2D0 hex, use the C command to write to the control register as follows:

BASEADDR=0x2D0; OUTPORTB(Baseaddr,0x98);

To read the inputs at Port A and the upper nybble of Port C, use the C INPUT command:

X=INP(BASEADDR)//Read Port AX= inportb(Baseaddr);Y=INP(BASEADDR+2)/16//Read Port C HiY= inportb(Baseaddr+2)>>4;

To set outputs high (1) at Port B and the lower nybble of Port C:

OUTPORTB(Baseaddr +1,0xFF);	//Turn on all Port B bits
OUTPORTB(Baseaddr +2,0x0F);	//Turn on all bits of Port C Lo 50

CONNECTOR PIN ASSIGNMENTS

A 50-pin ribbon cable header is used for interfacing to I/O. For mating connector, use an AMP type 1-746285-0 or equivalent. Connector pin assignments are listed below.

ASSIGNMENT	PIN	ASSIGNMENT	PIN
Port C Hi PC7* Port C Hi PC6 Port C Hi PC5 Port C Hi PC4	1 3 5 7	Ground " "	2 4 6 8
Port C Lo PC3** Port C Lo PC2 Port C Lo PC1 Port C Lo PC0	9 11 13 15	Ground " "	10 12 14 16
Port B PB7 Port B PB6 Port B PB5 Port B PB4 Port B PB3 Port B PB2 Port B PB1 Port B PB0	17 19 21 23 25 27 29 31	Ground " " " " "	18 20 22 24 26 28 30 32
Port A PA7 Port A PA6 Port A PA5 Port A PA4 Port A PA3 Port A PA2 Port A PA1 Port A PA0	33 35 37 39 41 43 45 47	Ground " " " " " "	34 36 38 40 42 44 46 48
+5 VDC	49	Ground	50

* This line is an I/O port and also an Interrupt Disable

** This line is an I/O port and also a User Interrupt

SPECIFICATION

Features

24 Input/Output Lines Four and Eight Bit Groups Independently Selectable for I/O Supports PPI Modes 0, 1, ans 2 Bi-Directional Data Transfers Supported. Interrupt and Interrupt-Disable Capability OPTO-22 Compatible 50-Pin Connector Compatibility with Module Mounting Racks +5 V Supply Available to User

Digital Inputs

Logic High: 2.0 to 5.0 VDC. Logic Low: -0.5 to 0.8 VDC. Input Load (Hi): +10 microamperes. Input Load (Lo): -10 microamperes.

Digital Outputs

Logic High: 2.5 VDC min., source 200 microamperes. Logic Low: 0.5 VDC max., sink 1.7 milliamperes.

Power Output: +5 VDC from computer bus, fused , 1A fast-blow.

Power Required: +5 VDC at 100 mA typical.

Size: 5.0" Long (127 mm).

Environmental

Operating Temperature Range: 0 degr. to 60 degr.C Storage Temperature Range: -50 degr. to 120 degr.C. Humidity: 0 to 90% RH, non-condensing.

WARRANTY

Prior to shipment, ACCES equipment is thoroughly inspected and tested to applicable specifications. However, should equipment failure occur, ACCES assures its customers that prompt service and support will be available. All equipment originally manufactured by ACCES which is found to be defective will be repaired or replaced subject to the following considerations.

TERMS AND CONDITIONS

If a unit is suspected of failure, contact ACCES' Customer Service department. Be prepared to give the unit model number, serial number, and a description of the failure symptom(s). We may suggest some simple tests to confirm the failure. We will assign a Return Material Authorization (RMA) number which must appear on the outer label of the package. All units/components should be properly packed for handling and returned with freight prepaid to the ACCES designated Service Center, and will be returned to the customer's/user's site freight prepaid and invoiced.

COVERAGE

First Three Years: Returned unit/part will be repaired and/or replaced at ACCES option with no charge for labor or parts not excluded by warranty. Warranty commences with equipment shipment.

Following Years: Throughout your equipment's lifetime, ACCES stands ready to provide on-site or in-plant service at reasonable rates similar to those of other manufacturers in the industry.

EQUIPMENT NOT MANUFACTURED BY ACCES

Equipment provided but not manufactured by ACCES is warranted and will be repaired according to the terms and conditions of the respective equipment manufacturer's warranty.

GENERAL

Under this Warranty, liability of ACCES is limited to replacing, repairing or issuing credit (at ACCES discretion) for any products which are proved to be defective during the warranty period. In no case is ACCES liable for consequential or special damage arriving from use or misuse of our product. The customer is responsible for all charges caused by modifications or additions to ACCES equipment not approved in writing by ACCES or, if in ACCES opinion the equipment has been subjected to abnormal use. "Abnormal use" for purposes of this warranty is defined as any use to which the equipment is exposed other than that use specified or intended as evidenced by purchase or sales representation. Other than the above, no other warranty, expressed or implied, shall apply to any and all such equipment furnished or sold by ACCES.

APPENDIX A PPI DATA SHEETS

The data sheets in this Appendix are provided to help your understanding of the 8255-5 PPI which is made by a number of companies. These sheets are reprinted with permission of Mitsubishi Electric Corp. (Copyright 1987).

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